Refine Search

Search Results -

Terms	Documents	
L13 and L50	52	

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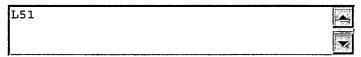
US Patents Full-Text Database

Database:

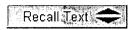
US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database

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Search:











Search History

DATE: Monday, January 30, 2006 Printable Copy Create Case

side by	Query	<u>Hit</u> Count	Set Name result
side	-DCDD LICHT LICAC EDAD IDAD DWDI TODO. DI LID-VEC. AD-AD		set
	=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR L13 and L50	52	T 51
<u>L51</u>		32	<u>L51</u>
<u>L50</u>	(return or flush\$3 or displac\$7 or evict\$3 or eject\$3) near (data or block to page or memory) with (shar\$3 or read)	4436	<u>L50</u>
<u>L49</u>	remain\$3 near sharer	3	<u>L49</u>
<u>L48</u>	L13 and L47	29	<u>L48</u>
<u>L47</u>	((read near "with" (modif\$6 or write)) with (store near instruction))	1689	<u>L47</u>
<u>L46</u>	((before near store near instruction) with (invalid\$5 or fail\$3))	5	<u>L46</u>
<u>L45</u>	((displac\$6 or remov\$3 or expel\$5 or eject\$3 or evict\$3) near (data or memory or copy) with (load near instruction))	18	<u>L45</u>
<u>L44</u>	L13 and L41	9	<u>L44</u>
<u>L43</u>	lock and L42	70	<u>L43</u>
<u>L42</u>	L39 and L41	106	<u>L42</u>
<u>L41</u>	(707/8 707/9 707/10).ccls.	6984	<u>L41</u>

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<u>L40</u>	L13 and L39	111	<u>L40</u>
<u>L39</u>	((exclusive or writ\$6) near ((owner) or (copy or memory or data or block)) with shar\$3)	4727	<u>L39</u>
<u>L38</u>	((no or "not") near (exclusive or writ\$6) near owner)	10	<u>L38</u>
<u>L37</u>	L31 and L36	6	<u>L37</u>
<u>L36</u>	L12 and L34	226	<u>L36</u>
<u>L35</u>	L13 and L34	11	<u>L35</u>
<u>L34</u>	(((execlusive or writ\$6) near (copy or block or memory or data)) with (load\$3 or stor\$3) near instruction)	1526	<u>L34</u>
<u>L33</u>	((invalid\$5 or fail\$3) near (load\$3 or stor\$3) near instruction)	74	<u>L33</u>
<u>L32</u>	L13 and L31	20	<u>L32</u>
<u>L31</u>	send\$3 near (victim or shar\$3)	691	<u>L31</u>
<u>L30</u>	(((writ\$5 or execlusive) with (load\$3 or stor\$3) near instruction) with (shar\$3 or victim))	67	<u>L30</u>
<u>L29</u>	victim adj to adj shar\$3	0	<u>L29</u>
<u>L28</u>	victim near shar\$3	51	<u>L28</u>
<u>L27</u>	L12 and L26	28	<u>L27</u>
<u>L26</u>	(((displac\$6 or remov\$3 or expel\$5 or eject\$3 or evict\$3) near (data or memory or copy) or (writ\$4 near back)) with (compar\$6 or match\$3 or determin\$3 or test\$3) near address)	116	<u>L26</u>
<u>L25</u>	((displac\$6 near (data or memory or copy) or writ\$4 near back) with (compar\$6 or match\$3 or determin\$3 or test\$3) near address)	98	<u>L25</u>
<u>L24</u>	L13 and L23	10	<u>L24</u>
<u>L23</u>	write near lock\$3	1845	<u>L23</u>
<u>L22</u>	displac\$6 near (data or memory or copy) with shar\$3	13	<u>L22</u>
<u>L21</u>	displac\$6 with exclusive near (block or copy)	2	L21
<u>L20</u>	L13 and L19	9	<u>L20</u>
<u>L19</u>	((replace\$6 or displac\$8) near (copy or memory or block) with shar\$3)	113	<u>L19</u>
<u>L18</u>	L16 and L17	2	<u>L18</u>
<u>L17</u>	(program or thread or task or process) near concurrent\$3	7231	<u>L17</u>
<u>L16</u>	(read\$3 and writ\$3) and L15	221	<u>L16</u>
<u>L15</u>	directory and L14	234	L15
<u>L14</u>	L12 and L13	294	L14
<u>L13</u>	distributed adj shared adj memory and (cache near (manage\$7 or coheren\$3))	540	L13
<u>L12</u>	L8 or L10 or L9 or L11	12763	<u>L12</u>
<u>L11</u>	(719/312 719/313 719/314).ccls.	768	<u>L11</u>
<u>L10</u>	(718/104 718/108).ccls.	1388	<u>L10</u>
<u>L9</u>	(711/117 711/118 711/119 711/120 711/121 711/122 711/123 711/124 711/125 711/126 711/127 711/128 711/129 711/130 711/131 711/132 711/133 711/134 711/135 711/136 711/137 711/138 711/139 711/140 711/141 711/142 711/143 711/144 711/145 711/146 711/147 711/148 711/149).ccls.	9072	<u>L9</u>
<u>L8</u>	(709/212 709/213 709/214 709/215 709/216).ccls.	2042	<u>L8</u>

<u>L7</u>	15 and L6	15	<u>L7</u>
<u>L6</u>	(Home or own\$3) near (cpu or processor)	5765	<u>L6</u>
<u>L5</u>	directory and ((parallel or concurrent\$3) near thread) and cache	156	<u>L5</u>
<u>L4</u>	directory and L3	17	<u>L4</u>
<u>L3</u>	load near lock and cache	190	<u>L3</u>
<u>L2</u>	load near lock and ((parallel or concurrent\$3) near thread) and cache	3	<u>L2</u>
<u>L1</u>	((parallel or concurrent\$3) near thread) with cache	43	<u>L1</u>

END OF SEARCH HISTORY